

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent No.: 7,549,208

Confirmation No. 9276

Issue Date: June 23, 2009

Application No. 10/687,753

Inventor: C. OKAMOTO et al.

Title: METHOD OF MOUNTING ELECTRONIC CIRCUIT CHIP

Customer No.: 86636

REQUEST FOR CERTIFICATE OF CORRECTION
PATENT OFFICE MISTAKE (37 CFR §1.322)

Director of the U.S. Patent and Trademark Office
P.O. Box 1450
Alexandria, VA 22313-1450

March 3, 2010

Sir:

Enclosed is a completed Certificate of Correction form. Please issue a Certificate of Correction showing the foreign application priority data and the international application information.

This information is correct on the cover of the patent, U.S. Patent No. 6,731,509, that issued from the parent application, U.S. Application No. 09/856,758.

Since the Certificate of Correction is necessitated by an error on the part of the Patent Office, no fee is deemed necessary. However, the Commissioner is hereby authorized to charge additional payment due or to credit any overpayment, to Deposit Account no. 50-4888.

Respectfully submitted,

BRUNDIDGE & STANGER, P.C.

By /Daniel J. Stanger/
Daniel J. Stanger
Registration No. 32,846
(703) 684-1470